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SUBJECT: CHANGE OF INTERLACE ON THE BUFFER DRUM

To: Staff, Groups 61, 64, and 6345

From: L. D. Healy

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Abstract: Changing the interlace on the auxiliary section of the buffer drum will increase the speed of drum transfers. This change makes it necessary that all programs using the buffer section of the drum be modified. This proposed change will be made June 4 unless there are objections.

At the present time, buffer drum block orders are done at the rate of a word every 64 μ seconds. It is possible to increase this rate, on the auxiliary storage section of the buffer drum, to a word every 32 μ seconds, thus saving computer time.

This change is complicated by the fact that the buffer section must continue to operate at its present rate. Therefore, it will be necessary to modify all programs using the buffer section. Programs must be changed so that the initial drum addresses for reading the various slots are as follows:


<u>Slot</u>	<u>Old Address</u>	<u>New Address</u>
0	0.00000	0.00000
1	0.00400	0.01000
2	0.01000	0.02000
3	0.01400	0.03000
4	0.02000	0.00001
5	0.02400	0.01001
6	0.03000	0.02001
7	0.03400	0.03001
10	0.04000	0.04000
11	0.04400	0.05000
12	0.05000	0.06000
13	0.05400	0.07000
14	0.06000	0.04001

<u>Slot</u>	<u>Old Address</u>	<u>New Address</u>
15	0.06400	0.05001
16	0.07000	0.06001
17	0.07400	0.07001

Programs using the auxiliary section of the buffer drum need not be changed.

The necessary changes in drum circuitry can be completed in one installation day. It is possible to do most of the wiring without affecting the system operation, allowing the actual conversion to be done in about one hour at some later time.

This change will be made on June 4 unless there are objections.



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LDH/jw